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Title of Invention:	Chip design command processor			
First Named Inventor/Applicant Name:	Khosro Khakzadi			
Correspondence Address:	Leo Peters LSI Logic Corporation MS D-106 1621 Barber Lane Milipitas CA 95035 US 7195337969			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)		
1		AAF 10 8 07.pdf	65601	yes	15		
		AAI _10_0_07.pai	e8243815923fed347f13070b519a4d7b 8691594a				
	Multipart Description/PDF files in .zip description						
	Document Description		Start	End			
	Amendment After Final		1	1			
	Claims		2	6			
	Applicant Arguments/Remarks	7	15				
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/ED/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.